

JCS75 U.S. PTO
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12/17/01

1073

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10022605	FILING DATE 12/17/2001	CLASS 257	SUBCLASS	GAU 2811	EXAMINER NGL/...
**APPLICANTS: Ferstl Klemens; Woerz Andreas; Vidal Ulrich;					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A CON OF PCT/DE00/01767 05/30/2000					
** FOREIGN APPLICATIONS VERIFIED: GERMANY 199 27 747.8 06/17/1999					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials				ATTORNEY DOCKET NO WMP-SME-352	
TITLE : Multichip module for LOC mounting and method for producing the multichip module <small>U.S. DEPT. OF COMM./PAT. & TM.-PTO-435L (Rev. 12-94)</small>					

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner	
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